

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number FIS920020139US1		Application Number 107538905
Applicant(s) H. Bernhard Pogge et al.		
Filing Date herewith	Group Art Unit not yet assigned	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/DG/		4,889,832	12/26/1989	Chatterjee			
/DG/		5,268,326	12/7/1993	Lesk et al.			
/DG/		6,489,217	12/3/2002	Kalnitsky et al.			
/DG/		5,091,331	2/25/1992	Delgado et al.			
/DG/		5,627,106	5/6/1997	Hsu			
/DG/		6,444,560	9/3/2002	Pogge et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/DG/		J.-Q. Lu et al., "Fabrication of via-chain test structures for 3D IC technology using dielectric glue bonding on 200 mm wafers," Materials Research Society ULSI XVII Conference Proceedings 151 (2002)
/DG/		P. Ramm et al., "Interchip via technology by using copper for vertical system integration," Materials Research Society Advanced Metallization Conference 159 (2002)

EXAMINER /David E. Graybill/	DATE CONSIDERED 08/18/2007
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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/DG/

Rahman et al., "Thermal analysis of three dimensional integrated circuits," IEEE International Interconnect Technology Conference Proceedings 157 (2001)

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